

**IN THE SPECIFICATION:**

The specification as amended below with replacement paragraphs shows added text with underlining and deleted text with ~~strikethrough~~.

Please REPLACE the paragraph beginning at page 4, line 7, with the following paragraph:

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In the display apparatus that uses a PDP, a frame is composed of plural subfields and the subfields to be lit are combined for each cell to represent the gradation scale. FIG.3 shows an example in which a frame is composed of the eight subfields SF1 through SF8. Each subfield comprises the reset period, the address period, and the sustain discharge period, respectively. There can be a case in which a difference appears in the total between the period of the display data supplied from the outside and that of all the subfields, and in such a case, a rest period is provided in the frame. For example, there are two methods for TV display, that is, the Vsync frequency can be 60 Hz or 50 Hz. If the plasma display apparatus is manufactured for 60 Hz and when the apparatus is used at 50 Hz, a ~~reset~~rest period is provided to adjust the period of a frame. In this ~~reset~~rest period, no display operation is performed and the length of the rest period is determined in accordance with the display data supplied from the outside. It may be a case where the length remains constant after being determined once, but there can be another case where the total number of pulses, that is, the sum of sustain pulses in all the cells in a frame, is controlled for power control, or another case where the number of the sustain pulses is adjusted in order to keep the brightness ratio among subfields constant regardless of the display load of each subfield, and so on, in other words, when the sustain period (light period) is varied, the length of the rest period is varied according to the display data. As described later, there may be a case where a reset period is not provided to some subfields to improve the display contrast or to abbreviate the reset period.